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Applicant : Debra Bernstein et al.                      Art Unit : Unknown  
Serial No. : Not yet assigned                      Examiner : Unknown  
Filed : August 19, 2003  
Title : MICROENGINE FOR PARALLEL PROCESSOR ARCHITECTURE

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Under 35 USC §120, this application relies on the earlier filing date of application serial number 09/387,046, filed on August 31, 1999. The attached references were submitted to and/or cited by the Office in the prior application and, therefore, are not provided in this application:

This statement is being filed with the application. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: \_\_\_\_\_

*August 19, 2003*

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Kenneth F. Kozik  
Reg. No. 36,572

Fish & Richardson P.C.  
225 Franklin Street  
Boston, MA 02110-2804  
Telephone: (617) 542-5070  
Facsimile: (617) 542-8906

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<b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Debra Bernstein et al.	
		Filing Date August 19, 2003	Group Art Unit

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	3,940,745	02/24/76	Sajeva, Raoul			
	AB	3,373,408	3/12/68	A.T. Ling			
	AC	5,630,130	5/13/97	Perotto et al.			
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	AI	6,216,220	4/2001	Hwang, Myeong Eun			
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AL	EP 0 745 933	12-4-96	EPO				
	AM	59111533	27-06-84	Patent Abstracts of Japan				
	AN							
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	AQ	"Multithreaded Processor Architectures", G.T. Byrd et al., IEEE Spectrum, IEEE Inc., New York, US, Vol. 32, No. 8, 8-1-95, pps. 38-46.
	AR	"A Processor Architecture For Horizon", M.R. Thistle et al., Orlando, Washington, IEEE Comp. Soc. Press., US, Vol. Conf. 1, 11-14-88, pps. 35-41.
	AS	"Strongarming Portable Communications", T. Litch et al., IEEE Micro, US, IEEE, Inc., New York, Vol. 18, No. 2, 3-1-98, pps. 48-55.
	AT	

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	